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istic never is better than -1, so the system never has any gain to compensate for noise. Exercise 1.81 The circuit functions as a buffer with logic levels $V_{IL} = 1.5$; $V_{IH} = 1.8$; $V_{OL} = 1.2$; $V_{OH} = 3.0$. It can receive inputs from LVCMOS and LVTTTL gates be- ... Digital Design and Computer Architecture, Exercise Solutions. SOLUTIONS = + + ...

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Behrooz Parhami's Textbook on Computer Architecture

2.3 The Evolution of the Intel x86 Architecture 44 2.4 Embedded Systems and the ARM 46 2.5 Performance Assessment 50 2.6 Recommended Reading and Web Sites 57 2.7 Key Terms, Review Questions, and Problems 59 PART TWO THE COMPUTER SYSTEM 63 Chapter 3 A Top-Level View of Computer Function and Interconnection 65 3.1 Computer Components 66

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